

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Yasushi AMAMIYA
Title: LOGIC CIRCUIT WITH RESTRAINED LEAK CURRENT
TO DIFFERENTIAL CIRCUIT
Appl. No.: 10/560,930
International Filing Date: 06/16/2004
371(c) Date: 12/15/2005
Examiner: An T. Luu
Art Unit: 2816
Confirmation Number: 4350

AMENDMENT AND REPLY UNDER 37 CFR 1.111

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

This communication is responsive to the Non-Final Office Action dated August 23, 2007, concerning the above-referenced patent application.

Amendments to the Claims are reflected in the listing of claims which begins on page 2 of this document.

Amendments to the Drawings begin on page 11 of this document, and include attached replacement drawing sheets.

Remarks/Arguments begin on page 12 of this document.

Please amend the application as follows: